

Abstract of the Disclosure:

In a method for amplitude trimming in transceivers having a PLL circuit operating on the two-point modulation principle, the amplitude of an analog modulation signal is selected on
5 the basis of a modulation shift of a defined digital modulation signal. A predetermined data sequence of the analog modulation signal is applied, the modulation shift of the analog modulation signal is determined, and the amplitude of the analog modulation signal is corrected to match the
10 difference between the modulation shift of the digital modulation signal and the determined modulation shift of the analog modulation signal.

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